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#### Comments:

### INITIAL FILING

Examiner: S. Lo Art Unit: 2128

U.S. Patent Application Serial No.: 10/772,434

Filing Date: February 6, 2004

Inventor(s): Ramachandran KRISHNASWAMY et al.

Title: METHOD AND STRUCTURE TO DEVELOP A TEST PROGRAM FOR

SEMICONDUCTOR INTEGRATED CIRCUITS

Docket No.: 333772000800

### Papers enclosed herewith:

Transmittal (1 page)

Fee Transmittal + duplicate copy for fee processing (2 pages)

Petition for Extension of Time - One Month (1 page)

Amendment (9 pages)

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